

CLAIMS

What is claimed is:

1 1. A computer-readable medium having stored thereon a
2 data structure comprising:

3 a first field containing data representing an
4 assigned target; and

5 a second field containing data representing a logic
6 value to be assigned to said assigned target.

1 2. The computer-readable medium of **claim 1**, wherein
2 said first field and said second field are incorporated
3 within a comment line of a design target entity source
4 code file.

1 3. The computer-readable medium of **claim 2**, further
2 comprising a pre-pended comment identifier field for
3 identifying said data structure as a hardware description
4 language comment.

1 4. The computer-readable medium of **claim 1**, wherein
2 said assigned target is a simulation event, and wherein
3 said first field further contains data declaring said
4 simulation event.

1 5. The computer-readable medium of **claim 4**, wherein
2 said first field further comprises an event type sub-
3 field containing data for declaring a type of simulation
4 event.

1 6. The computer-readable medium of **claim 1**, wherein
2 said assigned target is an intermediate signal, and
3 wherein said first field further contains data declaring
4 said intermediate signal.

1 7. The computer-readable medium of **claim 1**, wherein
2 said assigned logic value is a logical connectivity
3 expression for combining a plurality of signals.

1 8. The computer-readable medium of **claim 7**, wherein
2 said assigned target is an intermediate signal, and
3 wherein at least one of said plurality of signals
4 originates as said intermediate signal.

1 9. The computer-readable medium of **claim 1**, wherein
2 said assigned logic value is a logic function for
3 combining at least two signals.